

Figure 1

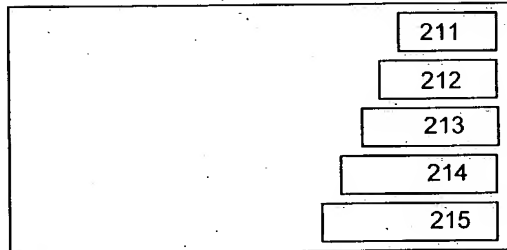


Figure 2A

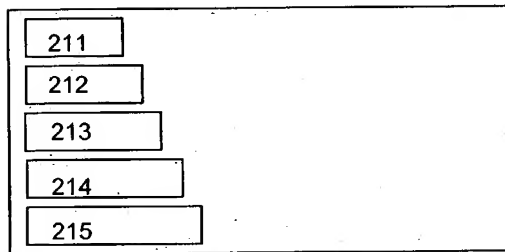


Figure 2B

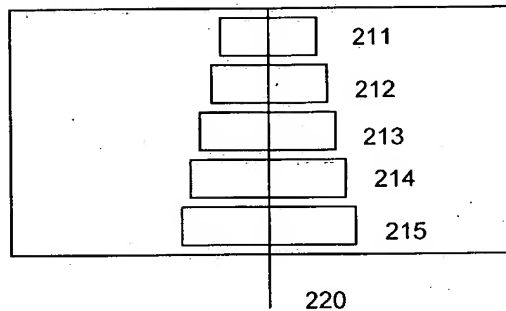


Figure 2C

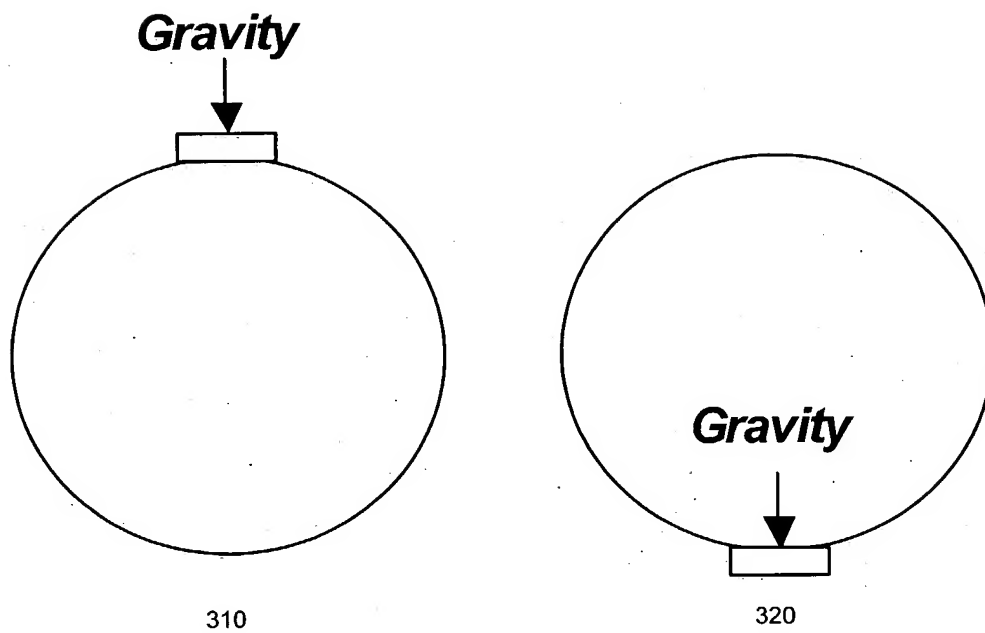


Figure 3

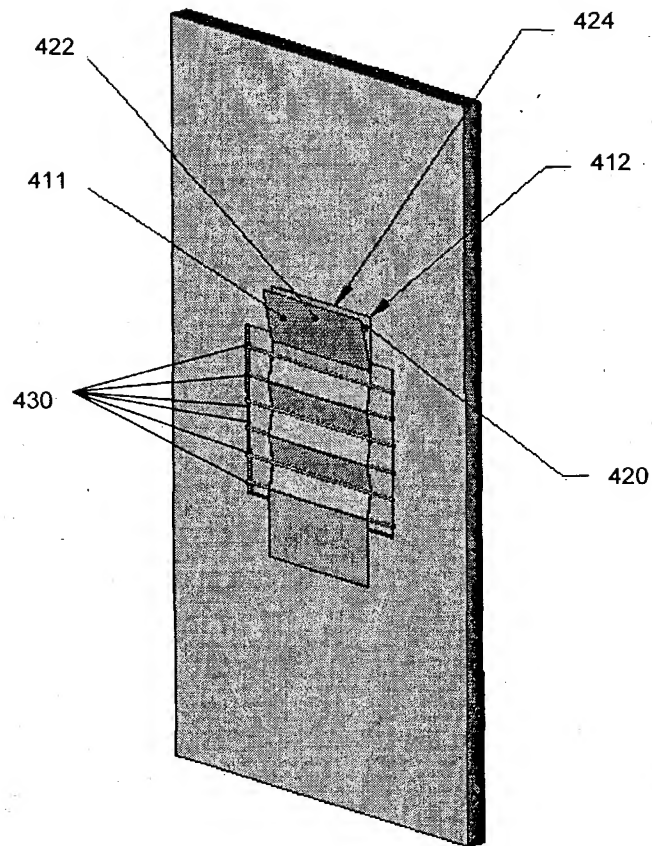


Figure 4

Sine Voltage Stack

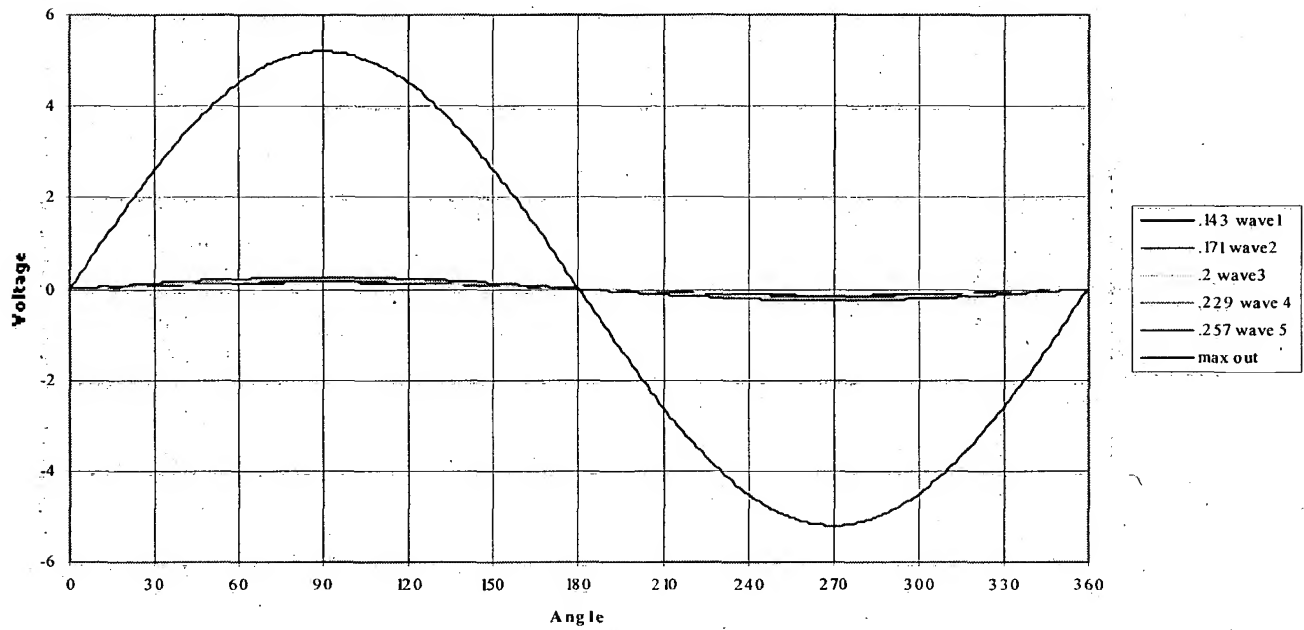


Figure 5

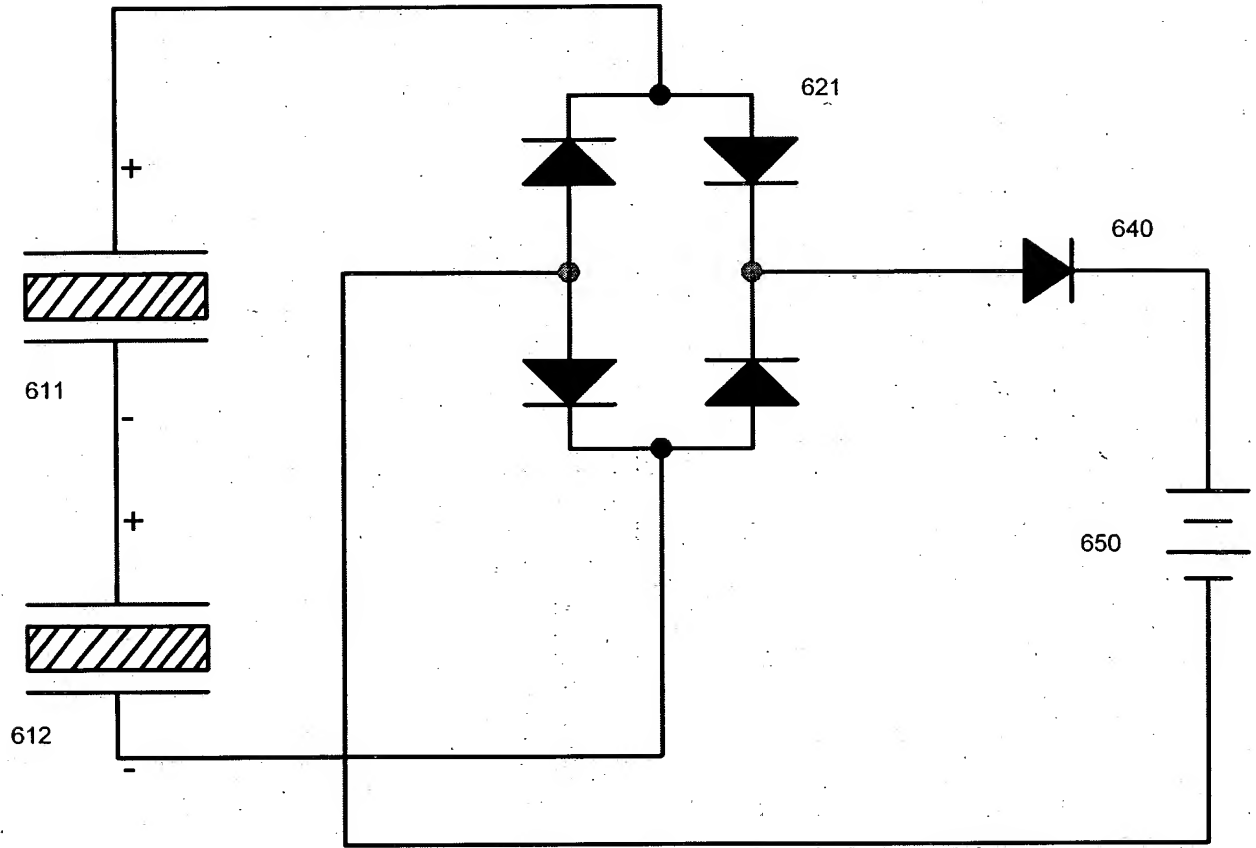


Figure 6